

GENERATING COMPONENTS ON A PROGRAMMABLE DEVICE USING A HIGH-LEVEL LANGUAGE

Abstract of the Disclosure

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Methods and apparatus are provided for implementing a programmable device including a processor core and a hardware accelerator. A portion of a program written in a high-level language is automatically selected for hardware acceleration. Dedicated ports are generated to allow the hardware accelerator to handle pointer
10 referencing and dereferencing. Profiling information is used to optimize selection of code for hardware acceleration.